REMARKS

Applicants respectfully request reconsideration of the application as amended.

Claims 1, 6-9, and 23 have been amended without introducing new matter. Claim 5 has

been canceled without prejudice. No claims have been added in the current response.

Claims 1-11 and 23-25 remain pending.

Claims 1-11 and 23-25 have been rejected under 35 U.S.C. §112, first paragraph,

as failing to comply with the enablement requirement. Specifically, the Examiner stated

that Applicant failed to adequately teach how to select a number of registers to be

modified by a second instruction whose execution is dependent on the result of

dependency producing instruction. Although Applicants do not necessarily agree with

the Examiner, Applicants have amended claims 1 and 23 to delete the language at issue

solely for the purpose of advancing prosecution. Thus, it is respectfully submitted that

the rejection has been overcome. Withdrawal of the rejection is respectfully requested.

Claims 1-11 and 23-25 have been rejected under 35 U.S.C. §112, second

paragraph as being indefinite for failing to particularly point out and distinctly claim the

subject matter which applicant regards as the invention. Accordingly, Applicant has

amended claims 1 and 23 to overcome the rejection. Withdrawal of the rejection is

respectfully requested.

Claims 1-11 and 23-25 have been rejected under 35 U.S.C. §102(b) as being

anticipated by Narad (US 5,284,503). Claim 5 has been canceled without prejudice, thus

obviating the rejection. Applicants respectfully traverse the rejections on claims 1-4, 6-

11, and 23-25.

Independent claim 1 as amended sets forth:

simultaneously reading original values from a *first* plurality of registers;

...

simultaneously writing a set of zero or more of the original values that have not

been modified and the modified values to a *second* plurality of registers distinct from the

first plurality of registers.

(Independent claim 1 as amended, emphasis added).

In contrast, Narad fails to disclose at least the above limitation. According to

Narad, the operating system will execute a read-modify-write cycle, whereby the CPU

reads information contained in the storage register, then modifies the contents and

subsequently writes the modified contents *back into the register*. (Narad, col. 1, ln. 39-

43). Narad does not disclose or suggest simultaneously writing a set of zero or more of

the original values that have not been modified and the modified values to a *second*

plurality of registers distinct from the first plurality of registers. Therefore, claim 1 as

amended is not anticipated by Narad for at least this reason. Withdrawal of the rejection

is respectfully requested.

For at least the reason discussed above with respect to claim 1, Narad does not

anticipate independent claim 23 as amended. Withdrawal of the rejection is respectfully

requested.

Claims 2-4, 6-11, and 24-25 depend, directly or indirectly, from claims 1 and 23,

respectively. Thus, claims 2-4, 6-11, and 24-25 are not anticipated by Narad for at least

the reason discussed above with respect to claims 1 and 23. Withdrawal of the rejection

is respectfully requested.

Applicants respectfully submit the present application is in condition for allowance. If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call C. Teresa Wong at (408) 720-8300.

Pursuant to 37 C.F.R. §1.136(a)(3), Applicants hereby request and authorize the U.S. Patent and Trademark Office to (1) treat any concurrent or future reply that requires a petition for extension of time as incorporating a petition for extension of time for the appropriate length of time and (2) charge all required fees, including extension of time fees and fees under 37 C.F.R. §§1.16 and 1.17, to Deposit Account No. 02-2666.

Respectfully submitted,

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